

FIG. 1 is a block diagram of a system 100 for transmitting and receiving data frames.

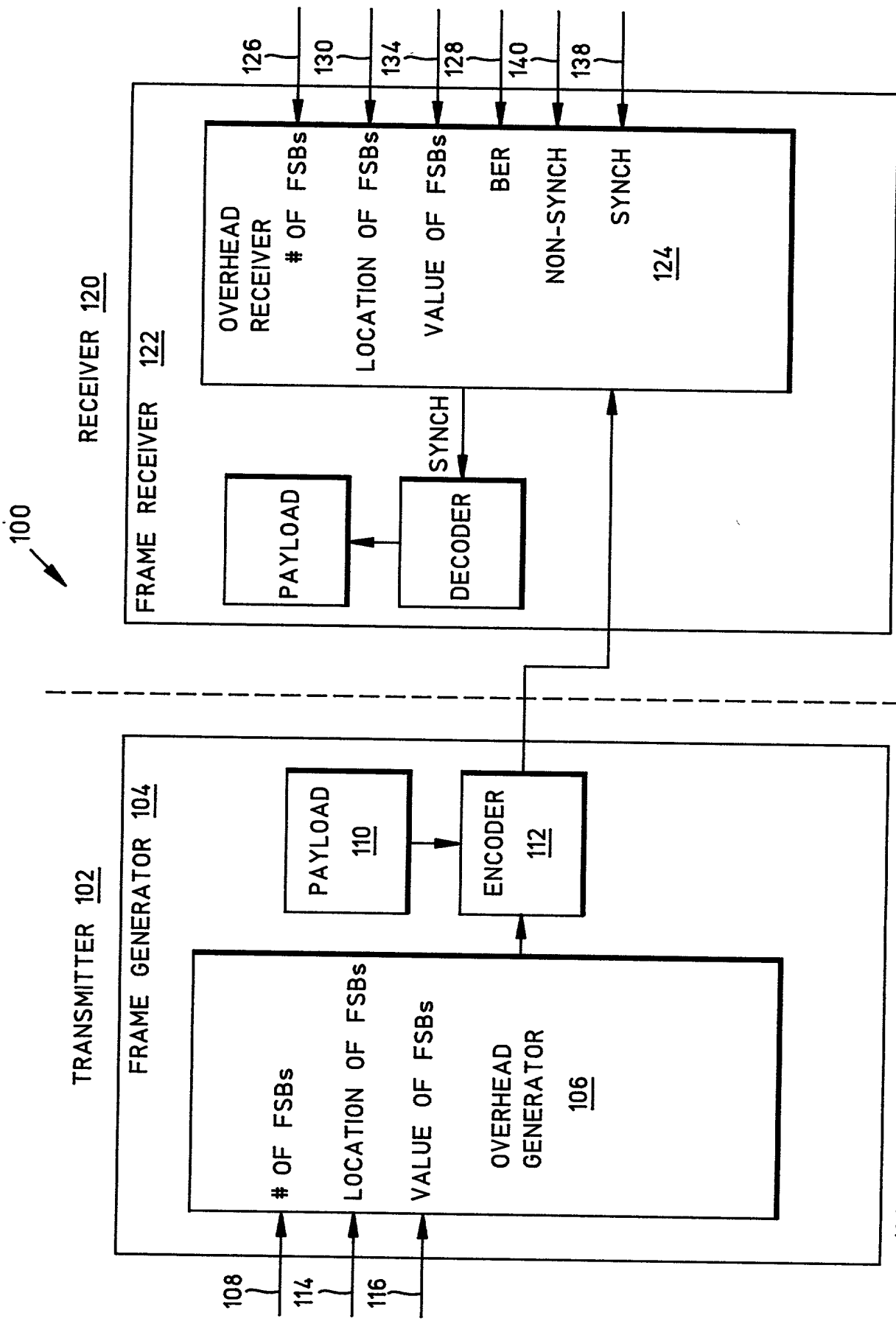


FIG. 1

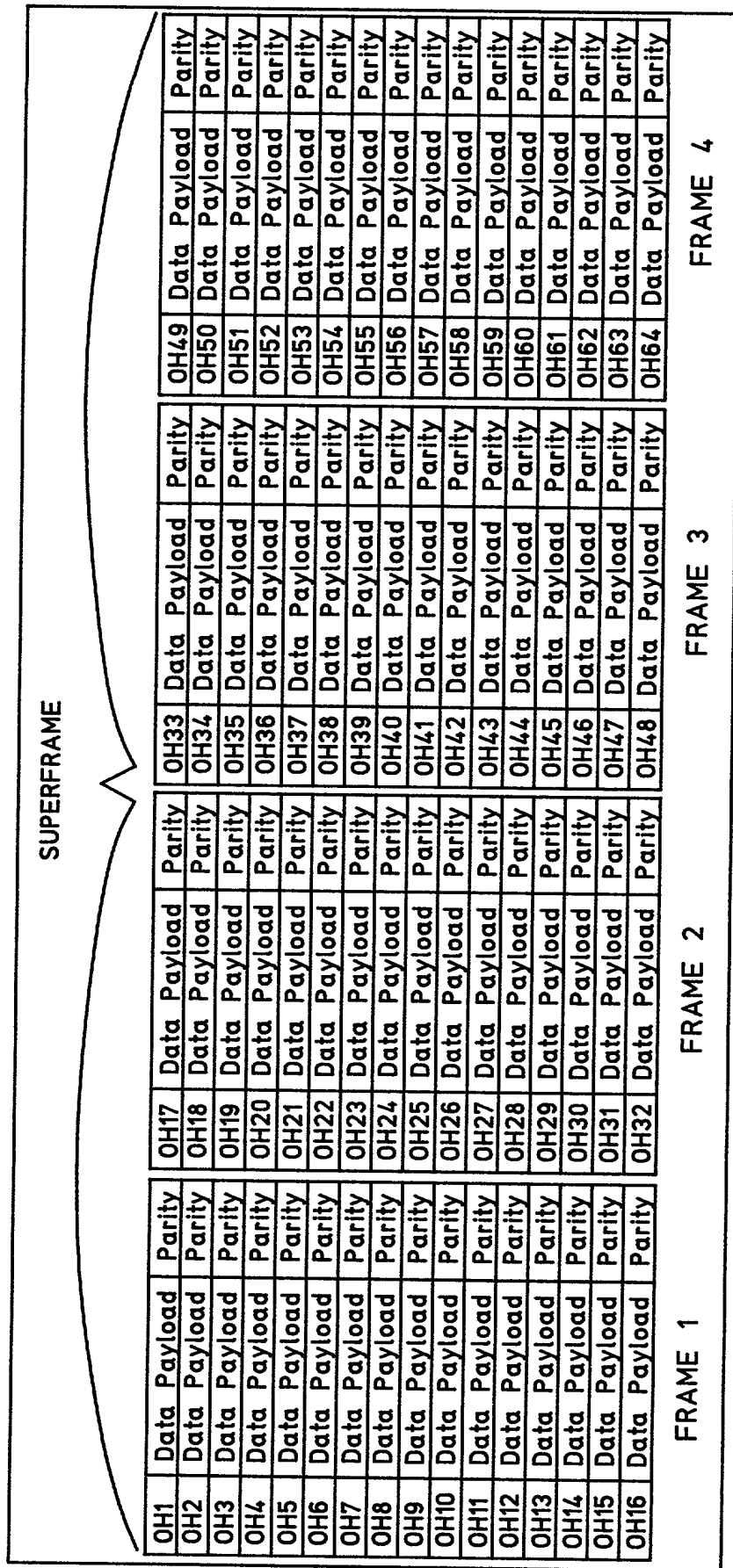


FIG. 2

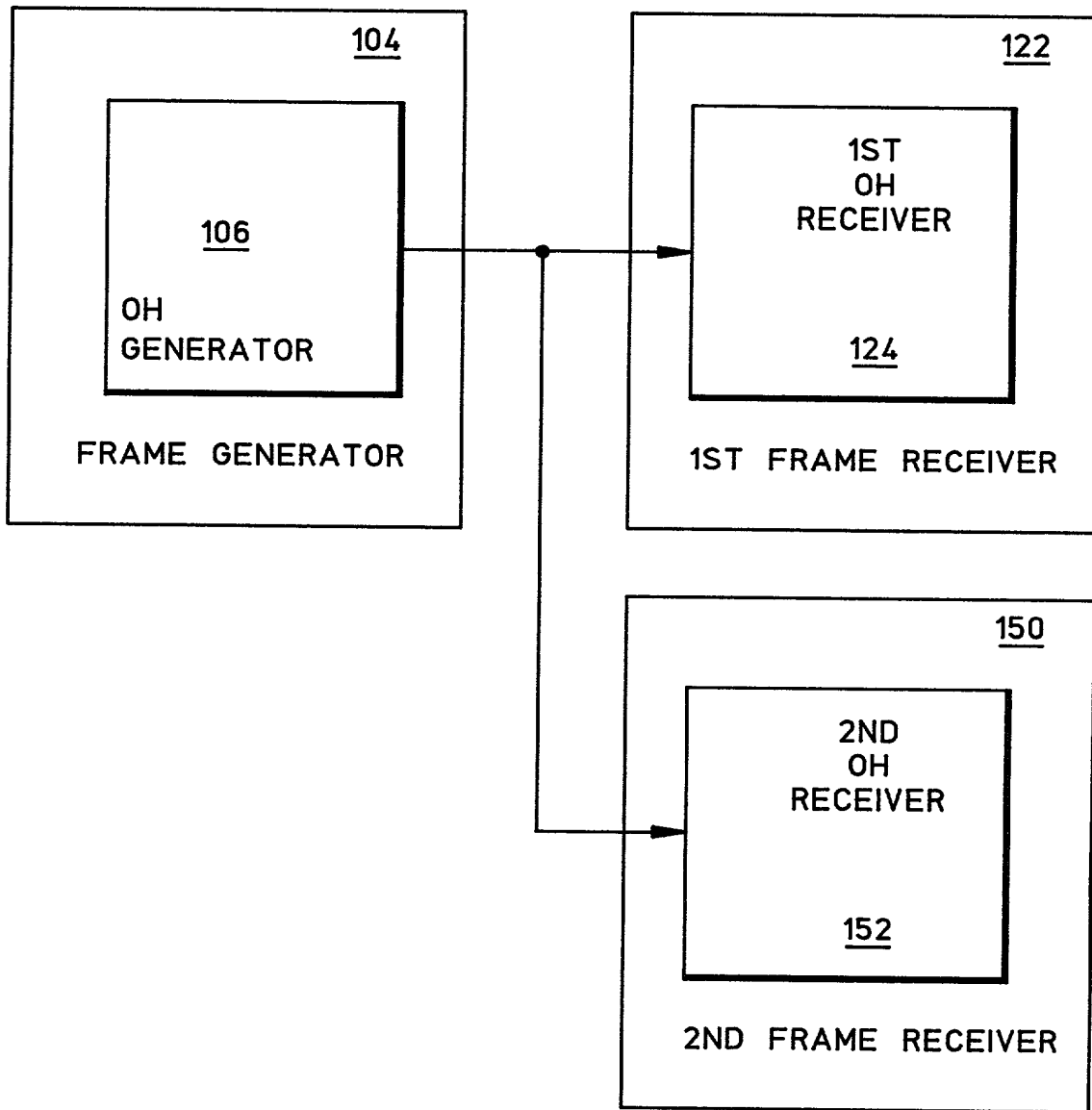


FIG. 3A

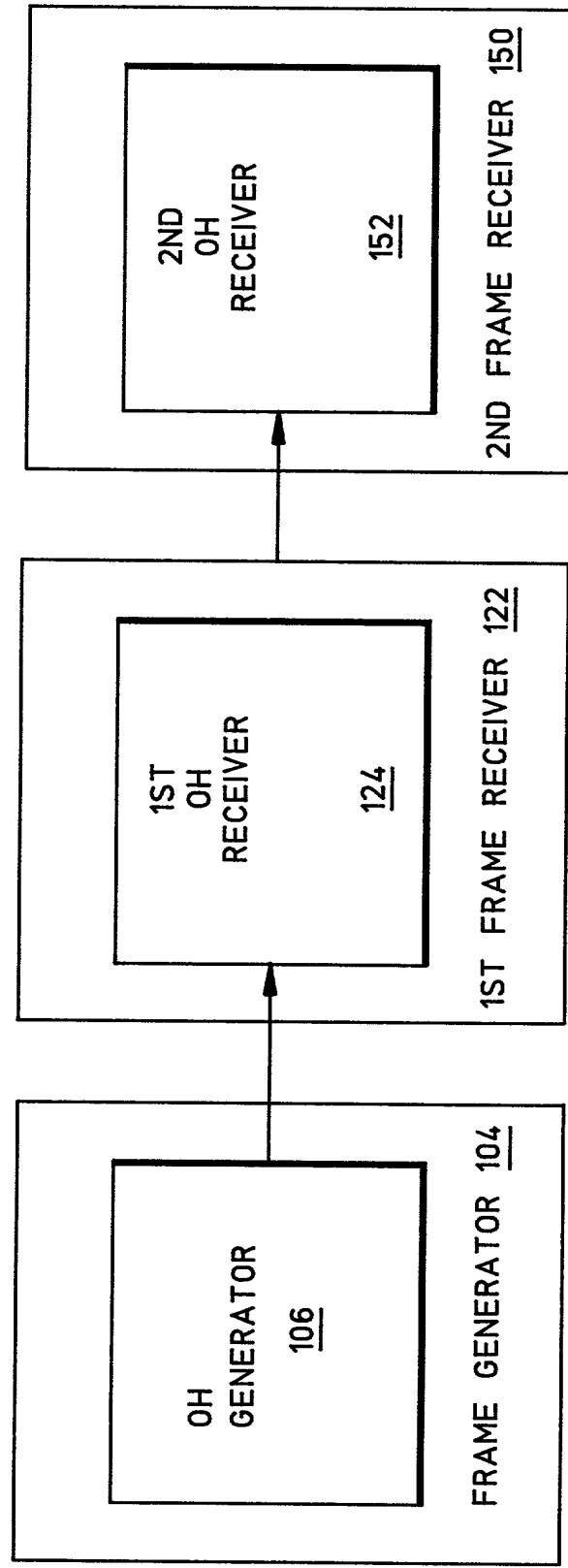
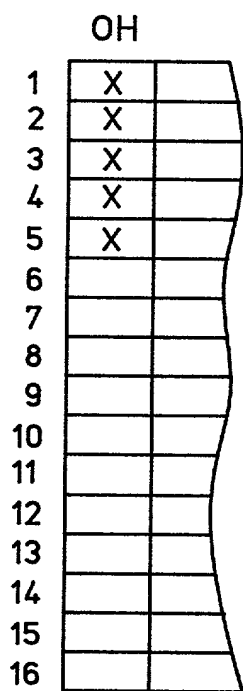
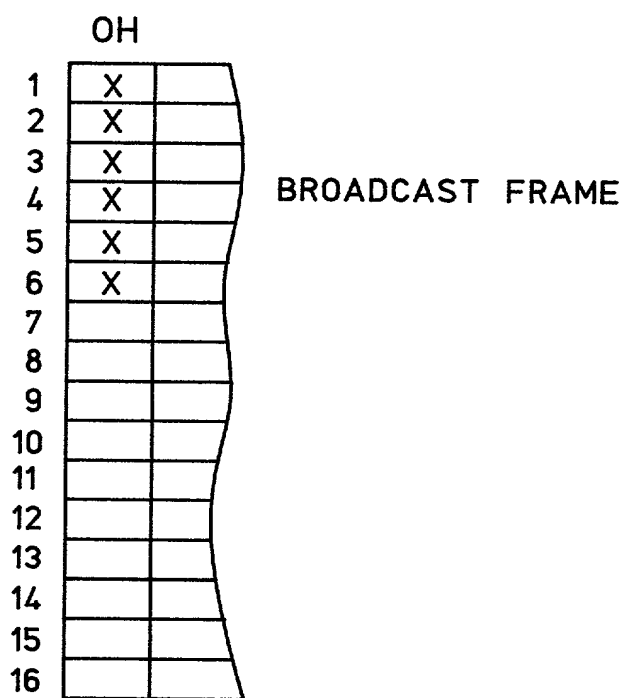
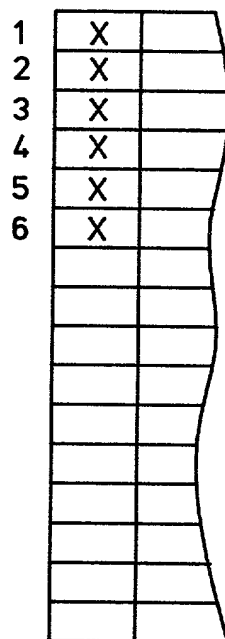


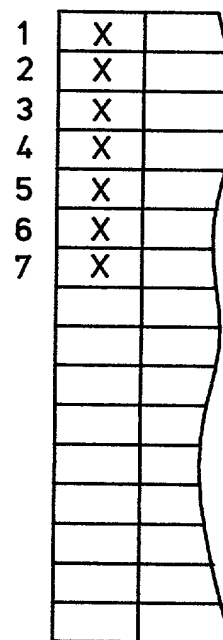
FIG. 3B



NODE 1

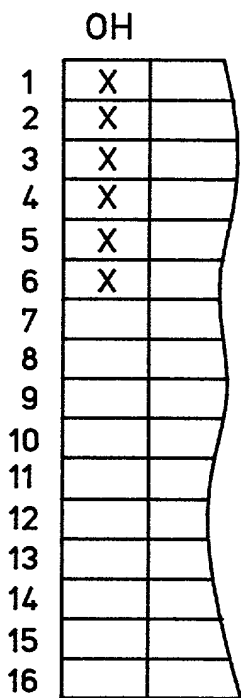
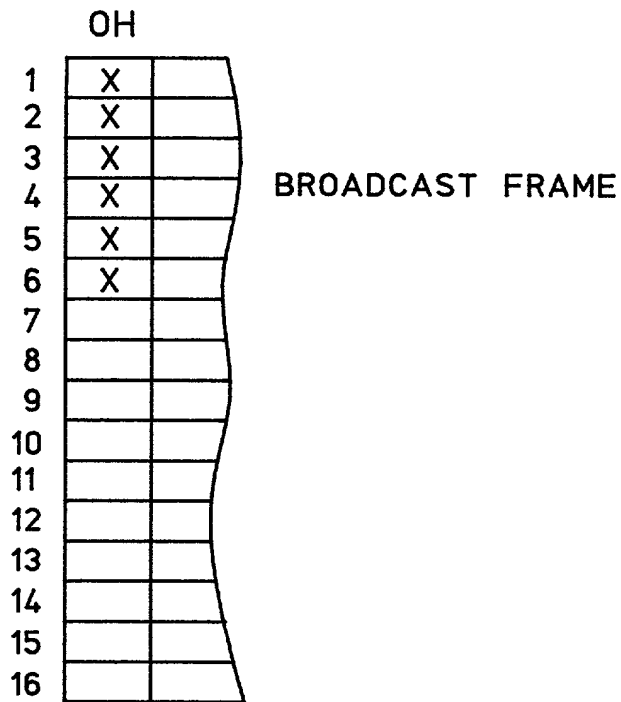


NODE 2

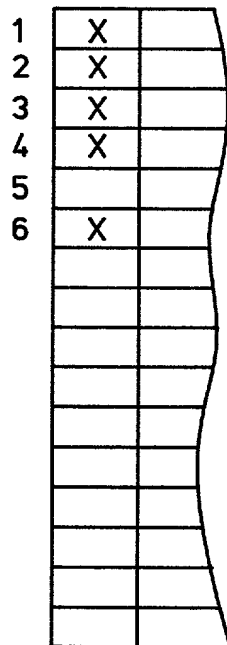


NODE 3

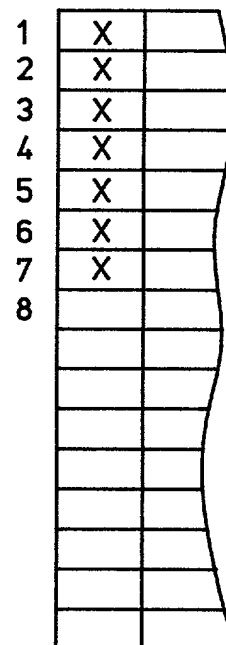
FIG. 4



NODE 1

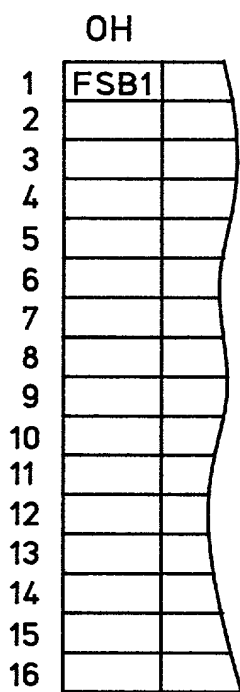
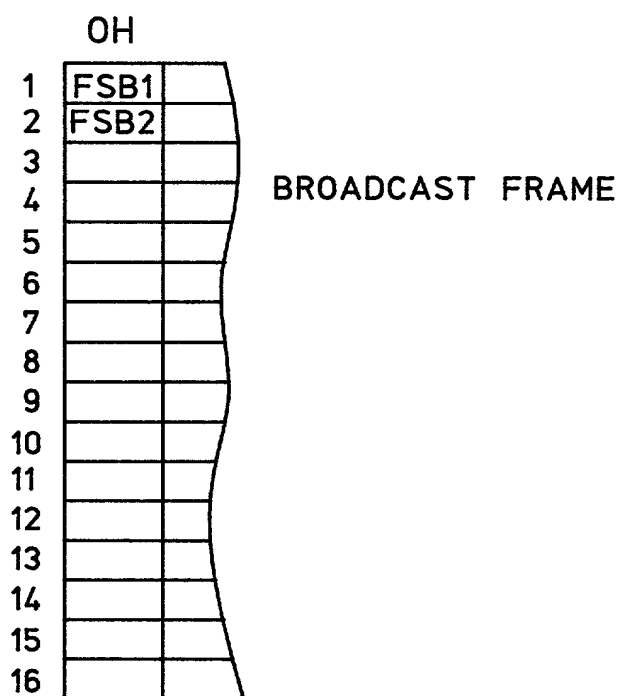


NODE 2

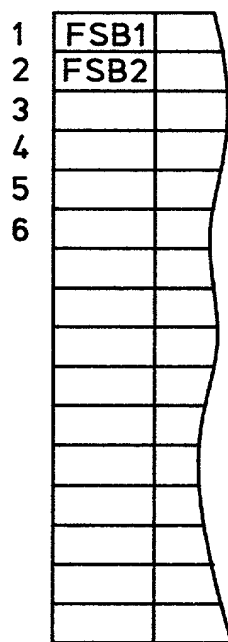


NODE 3

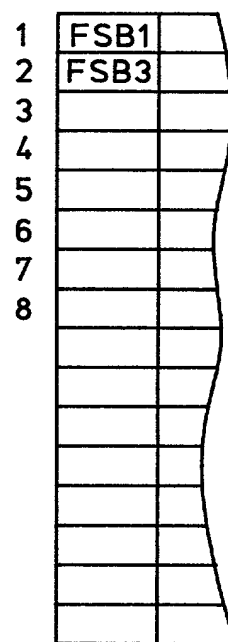
FIG. 5



NODE 1



NODE 2



NODE 3

FIG. 6

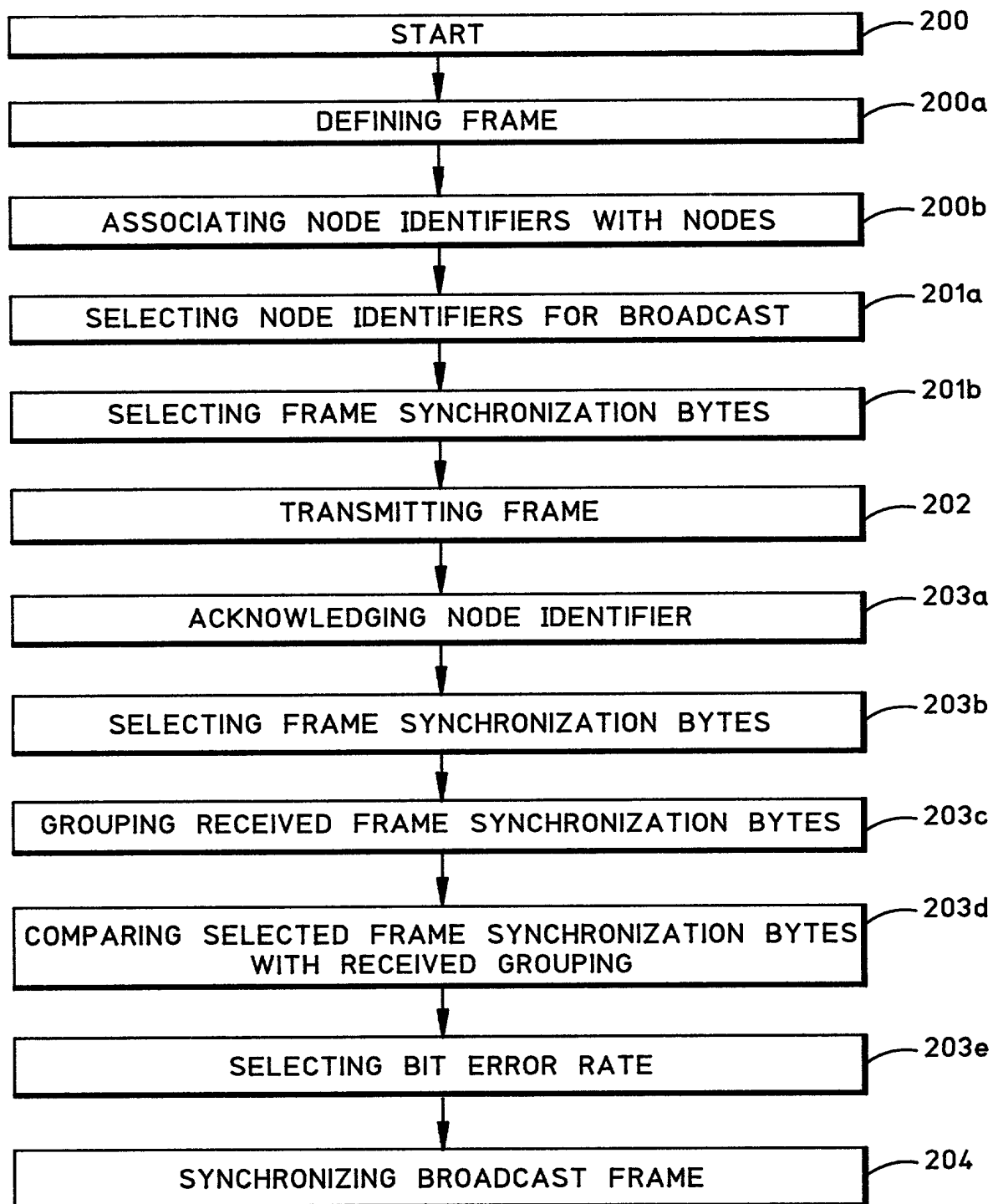


FIG. 7



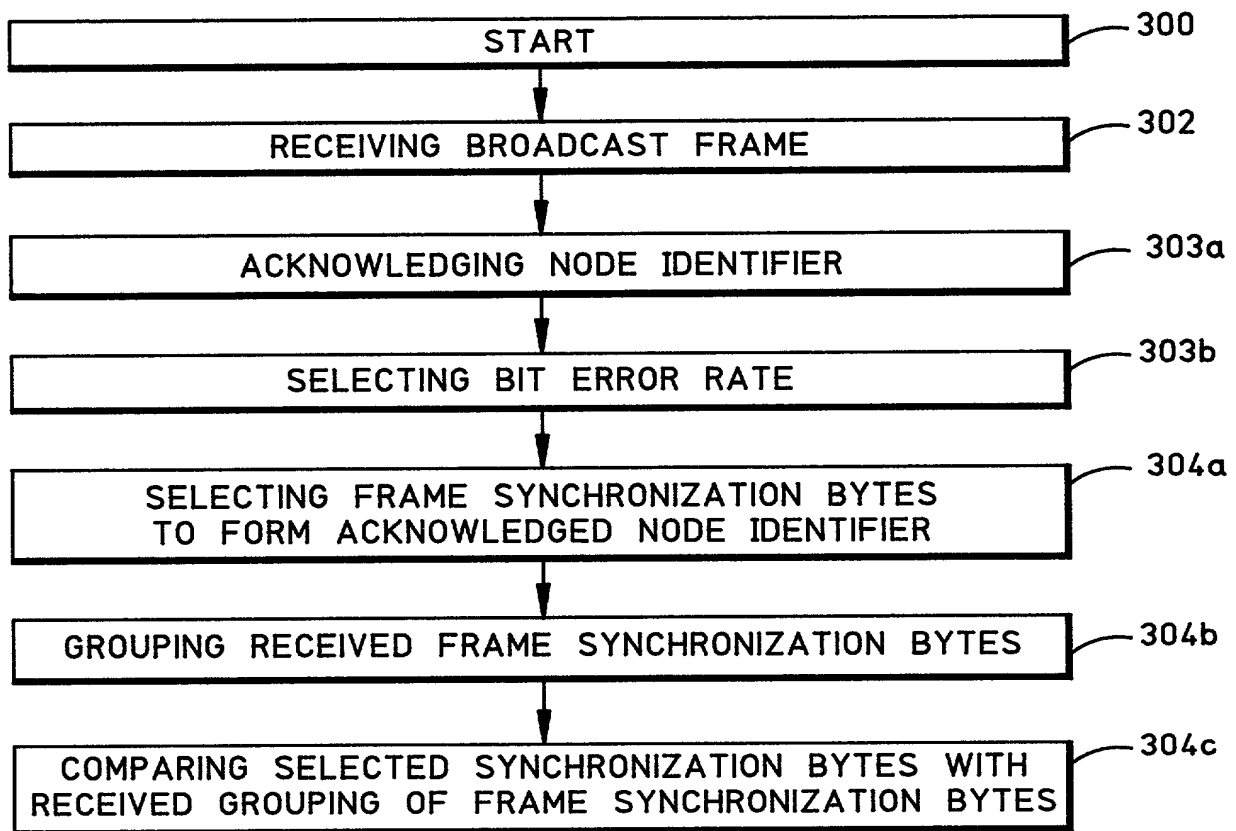


FIG. 8

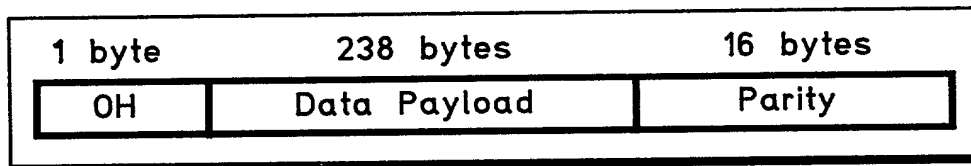


FIG. 9

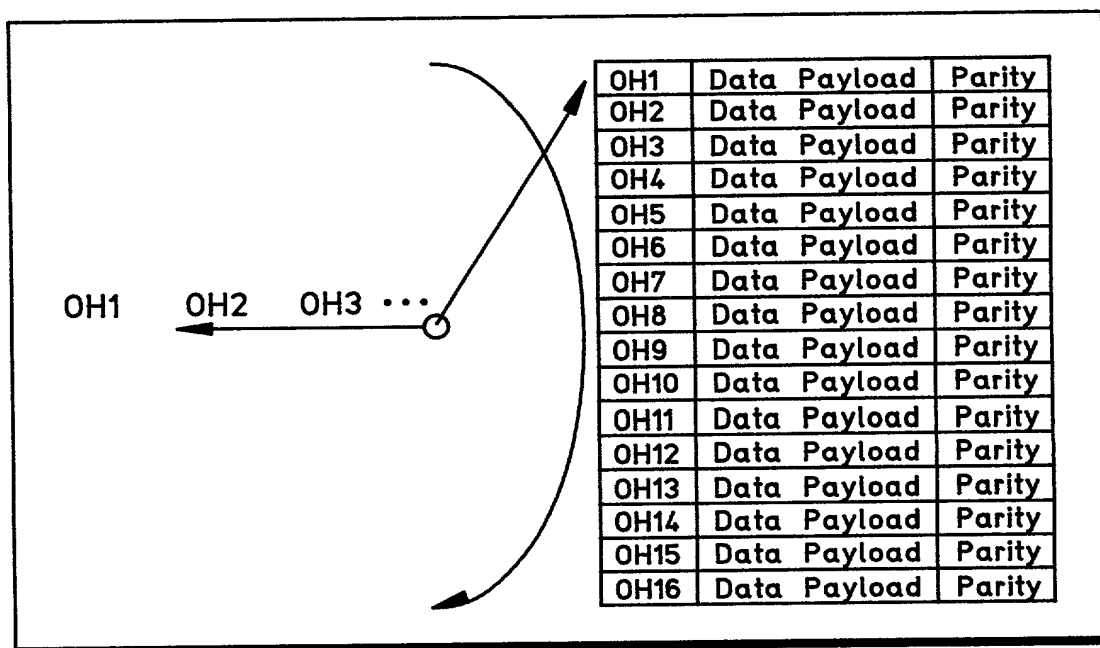


FIG. 10